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<b>3</b>	<b>630</b>	<b>((single adj chip) and CMOS) and pixel</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2002/09/20 14:56</b>
<b>4</b>	<b>81</b>	<b>((single adj chip) and CMOS) and pixel) and (timing adj circuit)</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2002/09/20 14:56</b>

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